

IN THE CLAIMS

Please amend the claims to read as indicated herein.

1. (currently amended) A method of testing an electrical switchgear system, comprising:
applying an analog signal to a node in said electrical switchgear system, wherein said node
monitors a power line signal and controls a breaker based on said power line signal,
and wherein said analog signal simulates said power line signal; and
receiving data ~~representing a status of said breaker~~ indicative of a response of said electrical
switchgear system to said analog signal.
2. (original) The method of claim 1, where in said data is received from at least one of said
node or said breaker.
3. (original) The method of claim 1, wherein said analog signal has a magnitude of less
than about 10 volts peak-to-peak.
4. (original) The method of claim 1, wherein said analog signal has a magnitude of about
2.5 volts peak-to-peak.
5. (original) The method of claim 1, wherein said analog signal has a magnitude of less
than or equal to about 10% of a magnitude of said power line signal.
6. (original) The method of claim 1, wherein said applying said analog signal is performed
while said node monitors said power line signal.
7. (original) The method of claim 1, wherein said analog signal simulates a fault condition
of said power line signal.
8. (original) The method of claim 1, wherein said analog signal simulates a non-fault
condition of said power line signal.

9. (currently amended) The method of claim 1, further comprising:

modifying said analog signal based on said ~~status of said breaker~~ response; and
receiving additional data representing ~~said status of said breaker~~ a further response of said
electrical switchgear system.

10. (original) The method of claim 1,

wherein said analog signal is a first analog signal, said node is a first node, said breaker is a
first breaker, and said power line signal is a first power line signal,

wherein said method further comprises applying, simultaneously with said applying said
first analog signal, a second analog signal to a second node in said electrical
switchgear system,

wherein said second node monitors a second power line signal and controls a second
breaker based on said second power line signal, and

wherein said second analog signal simulates said second power line signal.

11. (original) The method of claim 1, further comprising measuring a time required for the
said breaker to trip based on timestamps of said data.

12. (currently amended) A method of testing an electrical switchgear system, comprising:

applying a first analog signal to a first node in said electrical switchgear system, wherein
said first node monitors a first power line signal and controls a first breaker based on
said first power line signal, and wherein said first analog signal simulates said first
power line signal;

applying, simultaneously with said applying said first analog signal, a second analog signal
to a second node in said electrical switchgear system, wherein said second node
monitors a second power line signal and controls a second breaker based on said
second power line signal, and wherein said second analog signal simulates said
second power line signal; and

receiving data from said first node ~~representing a status of said first breaker~~ indicative of a
response of said electrical switchgear system to said first analog signal,

wherein said first analog signal has a magnitude of less than or equal to about 10% of a magnitude of said first power line signal.

13. (currently amended) An arrangement for testing an electrical switchgear system, comprising:

a generator for applying an analog signal to a node in said electrical switchgear system,

wherein said node monitors a power line signal and controls a breaker based on said power line signal, and wherein said analog signal simulates said power line signal; and

an interface for receiving data ~~representing a status of said breaker~~ indicative of a response of said electrical switchgear system to said analog signal.

14. (original) The arrangement of claim 13, wherein said interface receives said data from at least one of said node or said breaker.

15. (original) The arrangement of claim 13, wherein said analog signal has a magnitude of less than about 10 volts peak-to-peak.

16. (original) The arrangement of claim 13, wherein said analog signal has a magnitude of about 2.5 volts peak-to-peak.

17. (original) The arrangement of claim 13, wherein said analog signal has a magnitude of less than or equal to about 10% of a magnitude of said power line signal.

18. (original) The arrangement of claim 13, wherein said generator applies said analog signal while said node monitors said power line signal.

19. (original) The arrangement of claim 13, wherein said analog signal simulates a fault condition of said power line signal.

20. (original) The arrangement of claim 13, wherein said analog signal simulates a non-fault condition of said power line signal.

21. (currently amended) The arrangement of claim 13, wherein said arrangement: modifies said analog signal based on said ~~status of said breaker~~ response; and receives additional data representing ~~said status of said breaker~~ a further response of said electrical switchgear system.

22. (original) The arrangement of claim 13, wherein said analog signal is a first analog signal, said node is a first node, said breaker is a first breaker, and said power line signal is a first power line signal, wherein said generator is also for applying, simultaneously with said applying said first analog signal, a second analog signal to a second node in said electrical switchgear system, wherein said second node monitors a second power line signal and controls a second breaker based on said second power line signal, and wherein said second analog signal simulates said second power line signal.

23. (original) The arrangement of claim 13, further comprising a processor for measuring a time required for the said breaker to trip based on timestamps of said data.

24. (currently amended) A arrangement for testing an electrical switchgear system, comprising:
a generator for applying a first analog signal to a first node in said electrical switchgear system, wherein said first node monitors a first power line signal and controls a first breaker based on said first power line signal, and wherein said first analog signal simulates said first power line signal;
a generator for applying, simultaneously with said applying said first analog signal, a second analog signal to a second node in said electrical switchgear system, wherein said second node monitors a second power line signal and controls a second breaker

based on said second power line signal, and wherein said second analog signal simulates said second power line signal; and
an interface for receiving data from said first node ~~representing a status of said first breaker~~
indicative of a response of said electrical switchgear system to said first analog signal,
wherein said first analog signal has a magnitude of less than or equal to about 10% of a magnitude of said first power line signal.

25. (currently amended) A storage medium comprising instructions for controlling a processor for testing an electrical switchgear system to:
apply an analog signal to a node in said electrical switchgear system, wherein said node monitors a power line signal and controls a breaker based on said power line signal, and wherein said analog signal simulates said power line signal; and
receive data ~~representing a status of said breaker~~ indicative of a response of said electrical switchgear system to said analog signal.

26. (currently amended) A storage medium comprising instructions for controlling a processor for testing an electrical switchgear system to:
apply a first analog signal to a first node in said electrical switchgear system, wherein said first node monitors a first power line signal and controls a first breaker based on said first power line signal, and wherein said first analog signal simulates said first power line signal;
apply, simultaneously with said applying said first analog signal, a second analog signal to a second node in said electrical switchgear system, wherein said second node monitors a second power line signal and controls a second breaker based on said second power line signal, and wherein said second analog signal simulates said second power line signal; and
receive data from said first node ~~representing a status of said first breaker~~ indicative of a response of said electrical switchgear system to said first analog signal,
wherein said first analog signal has a magnitude of less than or equal to about 10% of a magnitude of said first power line signal.

Please add the following claims, newly numbered as claims 27 – 34.

27. (new) The method of claim 1, wherein said node continues to monitor said power line signal and control said breaker during said applying of said analog signal.

28. (new) The method of claim 12, wherein said first node continues to monitor said first power line signal and control said first breaker during said applying of said first analog signal.

29. (new) The arrangement of claim 13, wherein said node continues to monitor said power line signal and control said breaker during said applying of said analog signal.

30. (new) The arrangement of claim 24, wherein said first node continues to monitor said first power line signal and control said first breaker during said applying of said first analog signal.

31. (new) The storage media of claim 25, wherein said node continues to monitor said power line signal and control said breaker during said applying of said analog signal.

32. (new) The storage media of claim 26, wherein said first node continues to monitor said first power line signal and control said first breaker during said applying of said first analog signal.

33. (new) A system, comprising:

a node that monitors a power line signal and controls a breaker based on said power line signal;

a generator for applying an analog signal to said node, wherein said analog signal simulates said power line signal; and

an interface for receiving data indicative of a response of said electrical switchgear system to said analog signal.

34. (new) The system of claim 33, wherein said node continues to monitor said power line signal and control said breaker during said applying of said analog signal.